

Please type a plus sign (+) inside this box → ☒

PTO/SB/05 (4/98)
Approved for use through 09/30/2000. OMB 0651-0032
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 C.F.R. § 1.53(b))

Attorney Docket No. 120-25410

First Inventor or Application Identifier Jethro F. Steinman

Title SYSTEM AND METHOD FOR VALIDATING INTERFACES OF DYNAMICALLY LINKABLE COMPONENTS AND REAL-TIME PROCESS CONTROL SYSTEM ...

Express Mail Label No. EL053869385US

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

ADDRESS TO: Assistant Commissioner for Patents
Box Patent Application
Washington, DC 20231

1. ☒ * Fee Transmittal Form (e.g., PTO/SB/17)
(Submit an original and a duplicate for fee processing)
2. ☒ Specification [Total Pages 43]
(preferred arrangement set forth below)
 - Descriptive title of the Invention
 - Cross References to Related Applications
 - Statement Regarding Fed sponsored R & D
 - Reference to Microfiche Appendix
 - Background of the Invention
 - Brief Summary of the Invention
 - Brief Description of the Drawings (if filed)
 - Detailed Description
 - Claim(s)
 - Abstract of the Disclosure
3. ☒ Drawing(s) (35 U.S.C. 113) [Total Sheets 7]
4. Oath or Declaration [Total Pages 4]
 - a. ☒ Newly executed (original or copy)
 - b. ☐ Copy from a prior application (37 C.F.R. § 1.63(d))
(for continuation/divisional with Box 16 completed)
 - i. ☐ DELETION OF INVENTOR(S)
Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §§ 1.63(d)(2) and 1.33(b).

5. ☐ Microfiche Computer Program (Appendix)
6. Nucleotide and/or Amino Acid Sequence Submission
(if applicable, all necessary)
 - a. ☐ Computer Readable Copy
 - b. ☐ Paper Copy (identical to computer copy)
 - c. ☐ Statement verifying identity of above copies

ACCOMPANYING APPLICATION PARTS

7. ☒ Assignment Papers (cover sheet & document(s))
8. ☐ 37 C.F.R. § 3.73(b) Statement of Power of Attorney
(when there is an assignee)
9. ☐ English Translation Document (if applicable)
10. ☐ Information Disclosure Statement (IDS)/PTO-1449 [Copies of IDS Citations]
11. ☐ Preliminary Amendment
12. ☒ Return Receipt Postcard (MPEP 503)
(Should be specifically itemized)
13. ☐ * Small Entity Statement(s) filed in prior application, Status still proper and desired
(PTO/SB/09-12)
14. ☐ Certified Copy of Priority Document(s)
(if foreign priority is claimed)
15. ☐ Other:

* NOTE FOR ITEMS 1 & 13: IN ORDER TO BE ENTITLED TO PAY SMALL ENTITY FEES, A SMALL ENTITY STATEMENT IS REQUIRED (37 C.F.R. § 1.27), EXCEPT IF ONE FILED IN A PRIOR APPLICATION IS RELIED UPON (37 C.F.R. § 1.28).

16. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below and in a preliminary amendment:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No: _____

Prior application information: Examiner _____ Group / Art Unit: _____

For CONTINUATION or DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

17. CORRESPONDENCE ADDRESS

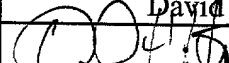
☒ Customer Number or Bar Code Label

000128

or ☒ Correspondence address below

(Insert Customer No. or Attach bar code label here)

Name	Anthony Miologos				
	Honeywell Inc.				
Address	Honeywell Plaza POB 524				
City	Minneapolis	State	MN	Zip Code	55440
Country		Telephone	(602) 313-5000	Fax	(602) 313-4559

Name (Print/Type)	David H. Hitt	Registration No. (Attorney/Agent)	33,182
Signature		Date	08/06/99

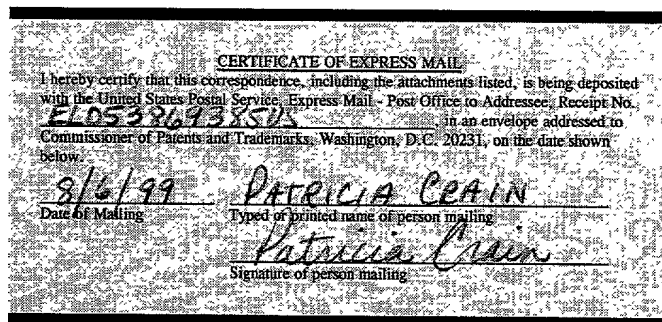
Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Box Patent Application, Washington, DC 20231.

SYSTEM AND METHOD FOR VALIDATING INTERFACES
OF DYNAMICALLY LINKABLE COMPONENTS AND REAL-TIME PROCESS
CONTROL SYSTEM INCORPORATING THE SAME

Inventors: Jethro F. Steinman
505 Dogwood Circle
Havertown, Pennsylvania 19083

Brian R. Reynolds
121 East Street
Philadelphia, Pennsylvania 19127

Assignee: Honeywell Inc.
Honeywell Plaza
Minneapolis, Minnesota 55408



David H. Hitt
Hitt Chwang & Gaines, P.C.
225 University Plaza
275 West Campbell Road
Richardson, Texas 75080
972-480-8800

**SYSTEM AND METHOD FOR VALIDATING INTERFACES
OF DYNAMICALLY LINKABLE COMPONENTS AND REAL-TIME PROCESS
CONTROL SYSTEM INCORPORATING THE SAME**

TECHNICAL FIELD OF THE INVENTION

The present invention is directed, in general, to real-time process control systems and, more specifically, to a system and method for validating interfaces of dynamically linkable components and real-time process control system incorporating the same.

BACKGROUND OF THE INVENTION

Real-time process control systems were first implemented within a single software program executing on a single computer system. As the need to monitor and control more physical devices increased, the number and the complexity of the software programs required accomplish this task also increased.

The earliest real-time process control software programs were constructed as a set of separate text constructs called source files. These source files were then transformed into object files and library files using software development tools. Library files contained library routines that were common to several tasks. The

use of the library files prevented the programmers from having to redevelop the same routines that had already been developed.

After the software files were written and compiled, the programmers joined them together in an operation called static linking. Static linking caused all of the application programs and the library routines to be joined into a single executable file. This executable file was the program that governed operating of the real-time process control system.

Under static linking, the software development tools informed programmers when an interface incompatibility existed between routines. This allowed the programmers to correct the incompatibility problems when someone changed the interface to one of the library routines. However, static linking required the programmers to re-link all the application programs and library routines whenever a change was made to any application program or library routine.

To correct the problem of having to re-link every time an application program or library routine changed, the software industry developed dynamic linking. Dynamic linking called for the library routines to remain separate from the application programs and stored in what is called Dynamically Linked Libraries ("DLLs"). Once a particular application program actually called a particular

library routine during runtime, the operating system (or the infrastructure services for embedded systems) dynamically linked the application program to the library routine. Once the application program and the library routine were dynamically
5 linked, the library routine could perform the desired function.

Under dynamic linking, when a library routine required an update, the programmers were only required to update the DLL and not the application programs that would employ the library routine during runtime. Once the DLL was updated, the application programs
10 could then use the updated DLL without having to perform another static linking. However, dynamic linking has its own problems.

One problem associated with dynamic linking is interface compatibility. Interface compatibility occurs when a calling software routine ("client") communicates to a called routine ("supplier") and the information passed is in the correct order and
15 of the correct type. If the information is disordered or of an incorrect type, then unpredictable results can occur, compromising the integrity and operation of the real-time process control system as a whole.

20 Interface incompatibility can occur when a routine's interface is changed in a DLL and the application programs that use that particular DLL are not correspondingly changed. This can occur

very easily, especially in complex real-time process control systems containing hundreds of application programs. What is needed in the art is a way to ensure that the interfaces of various cooperating application programs, modules or routines are compatible before the interfaces are relied upon.

5

663331 06060000

SUMMARY OF THE INVENTION

To address the above-discussed deficiencies of the prior art, the present invention provides a system for, and method of, validating an interface of a dynamically linkable component, such that one dynamically linkable component can assess its interface compatibility with another. In one embodiment, the system includes: (1) a check code generator that transforms the interface of the dynamically linkable component into an interface identifier representing the interface and couples the interface identifier to the dynamically linkable component and (2) a interface verifier that employs the interface identifier to determine a compatibility of the dynamically linkable component.

The present invention therefore introduces the broad concept of employing a number (an interface identifier) to allow quick and sure assessment of the interface compatibility of a given dynamically linkable component. The interface of the given component is transformed to generate the interface identifier. The present invention recognizes that, because dynamically linkable components are modular and assumed to be internally consistent, only changes to their interfaces are relevant to their interoperability.

It is important that the right amount of interface information be employed to create the interface identifier. If insufficient interface information is employed in generating the interface identifier, interfaces may appear compatible, when they are not (so-called alpha errors, or false positives). Concomitantly, if information not pertaining to an interface is included, interfaces may appear incompatible, when they are compatible (so-called beta errors, or false negatives). Thus, "transform" is defined for purposes of the present invention as a process in which adequate interface information is employed in creating an interface identifier such that alpha errors are eliminated and beta errors are substantially avoided.

In one embodiment of the present invention, the check code generator transforms the interface of the dynamically linkable component into the interface identifier by transforming a textual representation of at least a portion of the interface. In an embodiment to be illustrated and described, the interface, which can comprise a few thousand characters when represented in textual form, is transformed into a shorter interface identifier. The present invention is not limited to a specific technique for transforming the interface; all suitable conventional and later-discovered techniques (such as hashing) are within the broad scope

of the present invention.

In one embodiment of the present invention, the check code generator couples the interface identifier to the dynamically linkable component by placing the interface identifier in a types declaration file. A types declaration file is a file associable with a dynamically linkable component which declares the interface for the dynamically linkable component. The types declaration file also comprises other information. Those skilled in the pertinent art are familiar with the structure and use of types declaration files. Of course, the present invention can couple the interface identifier to the dynamically linkable component in any manner.

In one embodiment of the present invention, the interface identifier varies as a function of a version of the dynamically linkable component's interface. Alternatively, the interface identifier may be version-invariant, under the assumption that changes in a version do not involve changes in relevant interface capability.

In one embodiment of the present invention, the interface verifier employs the interface identifier to determine a compatibility of the dynamically linkable component with a second dynamically linkable component. Thus, peer components can verify their interface compatibility. Of course, the present invention

can advantageously allow an operating system to verify the interface compatibility of dynamically linkable components functioning in conjunction therewith, or vice versa.

In one embodiment of the present invention, the interface verifier is a part of a second dynamically linkable component. In this embodiment, each dynamically linkable component may include a interface verifier, allowing each to judge the interface compatibility of the others. Of course, the interface verifier may be placed in any suitable location.

In one embodiment of the present invention, the interface verifier determines a compatibility of the dynamically linkable component by comparing the interface identifier with a history list containing at least one member. Interfaces may be flexible, able to exchange information with more than one particular type of interface. Thus, the interface verifier may be adapted to do multiple comparisons.

In one embodiment of the present invention, the interface identifier is of a type selected from the group consisting of: (1) a check sum and (2) a cyclic redundancy check ("CRC"). In an alternate embodiment of the present invention, the interface identifier can be any number that is computed as a function of the appropriate interface text stream.

In one embodiment of the present invention, the check code generator uses filtering directives to include and exclude portions of the interface from the computation of the interface identifier. In another embodiment of the present invention, the check code generator may use other types of directives to generate an interface identifier.

In one embodiment of the present invention, the system includes: (1) an interface identifier coupled to a dynamically linkable component that represents an interface of the dynamically linkable component and (2) an interface verifier that employs the interface identifier to determine the compatibility of the dynamically linkable component. In another embodiment of the present invention, the interface identifier can be any number or textual information that is computed or manually determined and represents the interface of the dynamically linkable component.

The foregoing has outlined, rather broadly, preferred and alternative features of the present invention so that those skilled in the art may better understand the detailed description of the invention that follows. Additional features of the invention will be described hereinafter that form the subject of the claims of the invention. Those skilled in the art should appreciate that they can readily use the disclosed conception and specific embodiment as

a basis for designing or modifying other structures for carrying out the same purposes of the present invention. Those skilled in the art should also realize that such equivalent constructions do not depart from the spirit and scope of the invention in its
5 broadest form.

BRIEF DESCRIPTION OF THE DRAWINGS

For a more complete understanding of the present invention, reference is now made to the following descriptions taken in conjunction with the accompanying drawings, in which:

FIGURE 1 illustrates a block diagram of a real-time process control system that forms one environment within which the present invention can operate;

FIGURE 2 illustrates a block diagram of a controller's software programs and their associated dynamically linked application modules constructed according to the principles of the present invention;

FIGURE 3 illustrates a block diagram of generic and specific communication interfaces for dynamically linked application modules constructed according to the principles of the present invention;

FIGURE 4 illustrates a block diagram of a DLAM of FIGURE 3 of FIGURE 2 and its associated types declaration file constructed according to the principles of the present invention;

FIGURE 5 illustrates a block diagram of a check code generator and a interface verifier;

DETAILED DESCRIPTION

Referring initially to FIGURE 1, illustrated is a block diagram of a real-time process control system, generally designated 100, that forms one environment within which the present invention can operate. The real-time process control system 100 comprises a network 110 that interconnects a server 102, an operator interface 104, a field unit 106 and a controller 108. In the illustrated embodiment of the present invention, the real-time process control system 100 may comprise any number of servers 102, operator interfaces 104, field units 106 and controllers 108.

The network 110 comprises an industry standard network and industry standard network protocols. The industry standard network protocols, in one embodiment of the present invention, are ETHERNET® and Transmission Control Protocol/Internet Protocol ("TCP/IP"). In an alternate embodiment of the present invention, the network 100 comprises proprietary network and proprietary network protocols. In a third embodiment of the present invention, the network 100 may comprise a combination of industry standard and proprietary networks and network protocols. Wireless communications may also be used for all or part of the network communications.

The server 102 comprises software programs that monitor, process information, and control the physical devices within the real-time process control system 100. The software programs comprise a requesting program "client," and a resource program "supplier" and other miscellaneous programs. The client program sends requests to supplier programs to perform specific functions. The supplier programs receive requests and perform the appropriate functions based upon the type of requests sent. The client programs and supplier programs communicate over the network 110 or internally within the server 102.

The operator interface 104 comprises a computer and a display. The operator interface 104 displays information concerning the current state of the system 100. The operator interface 104 also accepts operator input to perform functions such as controlling a physical device or requesting other information to be displayed on the operator interface's 104 display. The operator interface 104 may comprise both client programs and supplier programs. The operator interface 104 communicates to other programs over the network 110.

The field unit 106 comprises supplier programs that perform tasks related to the physical devices that make up the real-time process control system 100. In one embodiment of the present

invention, the field unit's supplier programs collect status information, process data and control the physical devices. In other embodiments, the field unit 106 may perform more or less functions than described above. The field unit 106 responds to client's requests over the network 110. In another embodiment of the present invention, the field unit 106 comprises both supplier programs and client programs.

The controller 108 comprises software programs that perform specific tasks such as collecting status information, processing data and controlling physical devices. The software programs comprise a requesting program "client," and a resource program "supplier" and other miscellaneous programs. In other embodiments of the present invention, the controller 108 may perform additional or fewer functions than described. The controller 108 also responds to requests over the network 110. In another embodiment of the present invention, the controller 108 may comprise functions related to the server 102, the operator interface 104 or the field unit 106.

Referring now to FIGURE 2 illustrated is a block diagram of a controller's software programs and their associated dynamically linked application modules constructed according to the principles of the present invention. In the illustrated embodiment of the

present invention, each of the controller's software programs 210, 220, 230 perform specific tasks associated with a real-time process control system 100. The common routines used by each of the software programs 210, 220, 230 are in Dynamically Linked Application Modules ("DLAMs") 240, 250.

DLAMs are similar to Dynamically Linked Libraries ("DLLs") or Shared Libraries as used in commercially available operating systems. In concept, the DLAMs can use any linking scheme and any interface calling mechanism appropriate to the particular operating system or infrastructure services employed. DLAMs establish a dynamic link relationship with the calling software program in a method dependent upon the type of operating system used. DLAMs also allow a dynamic link relationship to be established in systems that use embedded operating systems. Those skilled in the pertinent art are familiar with the use of DLLs, Shared Libraries, dynamic linking and embedded operating systems.

The software program 210 establishes a dynamic link relationship 260 with Dynamically Linked Application Module ("DLAM") 240 when the software program 210 gains access to a software routine stored in DLAM 240. In this configuration, the software program 210 is requesting a service and is referred to as a client. The DLAM 240 is performing the service and is referred

to as a supplier. In another embodiment of the present invention, the software program 210 and the DLAM 240 are both a client and a supplier to each other.

The software program 220 establishes a dynamic link relationship 262 with DLAM 250. The software program 230 establishes dynamic links relationships 264, 266 with DLAMs 240, 250 respectively. The software programs 220, 230 are clients and DLAMs 240, 250 are suppliers. In another embodiment of the present invention, the software programs 220, 230 and the DLAMs 240, 250 are both clients and suppliers to each other.

Dynamic link relationships can also be established between DLAMs. In the illustrated embodiment of the claimed invention, DLAM 240 establishes a dynamic link relationship 270 with DLAM 250. Also, DLAM 250 establishes a dynamic link relationship 272 with DLAM 240. DLAMs 240, 250 are both clients and suppliers to each other.

Those skilled in the art should note that the present invention is not restricted to use in a controller. In another embodiment of the present invention, the invention may be used in controllers, servers and field units. The present invention is also not limited to the use of only two DLAMs. In other embodiments of the present invention, any number of software

programs may establish dynamic links with any number of DLAMs and each DLAM may establish dynamic links with more than one DLAM. Also, other embodiments of the present invention may contain more or fewer functional capabilities than described above.

5 Referring now to FIGURE 3, illustrated is a block diagram of generic and specific communication interfaces for dynamically linked application modules constructed according to the principles of the present invention. The Infrastructure Services ("IS") 350 is a set of services that provide the environment in which DLAMs may be loaded, linked and executed. In one embodiment of the present invention, the IS 350 is associable with a commercial operating system. In another embodiment of the present invention, the IS 350 is special purpose and is associable with an embedded system.

10 The dynamic linking between the IS 350 and DLAMs 310, 320, 330, 340 is accomplished by a Generic Infrastructure Communication Interface ("GICI"). A GICI is an interface in which there are a finite and fixed set of functions to be called by the IS 350 upon the DLAMs 310, 320, 330, 340 and by the DLAMs 310, 320, 330, 340 upon the IS 350. The GICI also comprises types declarations associated with the set of functions.

20 The DLAM1 310 and the DLAM2 320 communicate with each other

through a private communications interface called a Specific Application Communication Interface ("SACI"). The DLAM3 330 and the DLAM4 340 communicate with each other through an SACI. Changes to an SACI, or to any of the DLAMs 310, 320, 330, 340 which use the SACI, can be made without modification or release of the IS 350 or of the unrelated DLAMs. For example in FIGURE 3, changes to the DLAM1 310, the DLAM2 320 and the SACI A may be made without release of the DLAM3 330, the DLAM4 340 or the IS 350.

Those skilled in the art should note that the present invention is not restricted to use of only four DLAMs. In other embodiments of the present invention, any number of DLAMs may establish dynamic links with the IS 350 and to more than one DLAM. Also, other embodiments of the present invention may contain more or fewer functional capabilities then described above.

Referring now to FIGURE 4, illustrated is a block diagram of a DLAM of FIGURE 2 and its associated types declaration file constructed according to the principles of the present invention. The DLAM 410 comprises multiple routines used by the controller 108 and other components of the real-time process control system 100. One such routine is FUNCTION_A 412.

The types declaration file 420 is associated with the DLAM 410. The types declaration file 420 contains the interface

descriptions for the routines contained within the DLAM 410.
FUNCTION_A's interface description 422 shows the interface
description associated with the software code for the FUNCTION_A
412. FUNCTION_A's interface description 422 can include the
5 description of the routine, the comments concerning updates, the
interface version description, the interface calling parameters,
the interface identifier and the interface identifier history.

Also included in FUNCTION_A's interface description are
embedded filtering directives. Filtering directives are used by a
10 check code generator to include or exclude specific sections of the
interface description in the interface identifier computation. See
FIGURE 5 for a more detailed description of the check code
generator. In another embodiment of the present invention,
filtering directives are not used and the interface identifier may
15 be derived from information other than a check code.

The check code generator transforms FUNCTION_A's interface
description 422 into an interface identifier that represents the
signature of FUNCTION_A's interface. In another embodiment of the
present invention, the check code generator transforms multiple
20 interface descriptions and other relative information within the
types declaration file 420 into an interface identifier.

The check code generator stores the interface identifier in

the types declaration file 420. The check code generator also maintains a history of the interface identifiers associated with each version of the interface. Those skilled in the pertinent art are familiar with the structure and use of types declaration files.

5 In another embodiment of the present invention, the interface identifier may be manually inserted into the types declaration file 420. In other embodiments of the present invention, the present invention can couple the interface identifier to the DLAM in any manner.

10 Those skilled in the art should note that the present invention is not restricted to a single function or to the function's interface information described above. Other embodiments of the present invention may contain more or less interface information than described.

15 Table 4-1 shows an example of software code for a routine's interface description.

TABLE 4-1 Interface Description Software Code

```
// begin *****
// contract.h - source declaration file for SACI *****

//Filtering_Directive:_IncludeLinesBegin

struct strAccessArgument1 {
    :
    // data declarations appropriate to access argument 1
    :
}

struct strAccessArgument2 {
    :
    // data declarations appropriate to access argument 2
    :
}

struct strContractFPT {
    BOOLEAN (*ValidateInterface) (UINT32);
    UINT32 (*Access) (strAccessArgument1, strAccessArgument2);
}

//Filtering_Directive:_IncludeLinesEnd

// Interface Identifier
#define CLIENT_INTERFACE_ID 0x1A6EC029

// Interface Identifier History
#define SUPPLIER_INTERFACE_ID_SET\
{\
3\
0x1A6EC029,\
0xDC035B72,\
0x236AF731\
}
```


Referring now to FIGURE 5, illustrated is a block diagram of a check code generator and a interface verifier. The check code generator 510 transforms a routine's interface information into an interface identifier to allow the quick and sure assessment of the interface compatibility of a given dynamically linkable routine or component. Because the dynamically linkable routines or components of a DLAM are modular and assumed to be internally consistent, only the changes to the routine's or component's interfaces are relevant to their interoperability.

It is important that the right amount of interface information be employed to create the interface identifier. If insufficient interface information is employed in generating the interface identifier, interfaces may appear compatible, when they are not (so-called alpha errors, or false positives). Concomitantly, if information not pertaining to an interface is included, interfaces may appear incompatible, when they are compatible (so-called beta errors, or false negatives). Thus, the check code generator 510 uses adequate interface information to create an interface identifier such that alpha errors are eliminated and beta errors are substantially avoided.

In one embodiment of the present invention, the check code generator 510 employs filtering directives to allow the check code

generator 510 to include and exclude textual interface information in the generation of the interface identifier. In a second embodiment of the present invention, the check code generator 510 also filters out the white space, blank text areas that do not express any semantics of the interface information, from the interface identifier computation.

In one embodiment of the present invention, the check code generator 510 calculates an interface identifier that is a check sum. In an alternate embodiment of the present invention, the check code generator 510 calculates an interface identifier that is cyclic redundancy check ("CRC"). Those skilled in the pertinent art are familiar with the generation and use of both the check sum and the CRC. Other methods of generating an interface identifier are within the scope of this invention.

In one embodiment of the present invention, the check code generator 510 maintains an interface identifier history list. The interface identifier history list comprises a list of interface identifiers representing the history of the modifications to the interface. In a second embodiment of the present invention, the check code generator 510 also uses "grandfathering." Under grandfathering, particular function signatures are never changed. If a modification is required to a function, the old function is

declared obsolete, but is still supported in the interface. This allows the programs already in the field to maintain compatibility with the updated DLAM.

Associated with the check code generator 510 is the interface verifier 520. The interface verifier 520 employs the interface identifier generated by the check code generator 510 to determine the compatibility of a dynamically linkable routine or component within a DLAM. If the interface identifier stored for a particular dynamically linkable routine or component does not match the calling routine's interface identifier, then the calling routine can safely handle the incompatibility without causing damage to the real-time process control system's physical devices. Without the use of interface identifiers to determine interface compatibility, the calling routines would blindly execute the dynamically linkable routine producing undesirable and unknown results.

In one embodiment of the present invention, the interface verifier 520 is a separate routine apart from the DLAMs. In a second embodiment of the present invention, the interface verifier 520 is part of a DLAM and is used by the DLAM to verify interface compatibility of other DLAM routines or components within the same DLAM or in other DLAMs. In another embodiment of the present invention, the interface verifier 520 uses the interface identifier

history list and grandfathering to allow software programs to maintain compatibility with previous versions of DLAMs.

One skilled in the art should know that the present invention is not limited to interface compatibility verification. In another embodiment of the present invention, the check code generator 510 and the interface verifier 520 may be used to verify compatibility of other aspects of the real-time process control system 100. Also, other embodiments of the present invention may have additional or fewer steps or procedures than described above.

Referring now to FIGURE 6, illustrated is a flow diagram of a method of generating an interface identifier from a DLAM routine's interface information of FIGURE 4. In FIGURE 6, the check code generator 510 first performs initialization in a step 602.

After initialization, the check code generator 510 retrieves text from the routine's interface information in a step 604. The check code generator 510 determines if the text retrieved is a filtering directive code to start including text in calculating an interface identifier in a decisional step 606.

If the retrieved text is an include code filtering directive, the check code generator 510 retrieves text to be included in the interface identifier calculation in a step 610. The check code generator 510 then determines if the retrieved text is an end

include code filtering directive in a decisional step 612.

The use of filtering directive allows the check code generator 510 to exclude portions of the interface information not pertaining to the interface from the interface identifier calculation. This prevents the so-called beta errors or false negatives. For example in FIGURE 4, FUNCTION_A's interface description 422 contains a section that describes who modified the routine and when it was last modified. This section is an example of one type of information that would create a beta error or false negative upon subsequent modifications.

If the retrieved text is not an end include code filtering directive, then the check code generator 510 uses the retrieved text to calculate a check code in a step 614. A check code is a calculated number that is the interface identifier for a DLAM routine's interface. In one embodiment of the present invention, the check code calculation is a check sum. In an alternate embodiment of the present invention, the check code calculation is a CRC. Other methods of generating an interface identifier are within the scope of this invention. The check code generator then retrieves more text to be included in the calculation in the step 610.

If the retrieved text is an end include code filtering

directive, the check code generator 510 determines if there is more text in the interface to process in a decisional step 620. If there is more interface text to process, the check code generator 510 returns to retrieve additional text in the step 604.

5 If there is no additional interface text to process, the check code generator 510 stores the check code as the interface identifier in the associated types declaration file 420. In another embodiment of the present invention, the check code generator 510 couples the interface identifier to the dynamically linkable routine or component in any manner. In a third embodiment of the present invention, the interface identifier may be manually inserted in the types declaration file 420.

10 One skilled in the art should know that the present invention is not limited to the textual interface information described in FIGURE 4 to generate an interface identifier. Nor is the present invention limited to interface identifiers based on a check code. Also, the present invention is not limited to the filtering directives described above. Another embodiment of the present invention may use different filtering directives or no filtering directives. Also, other embodiments of the present invention may have additional or fewer steps than described above.

20 Referring now to FIGURE 7, illustrated is a flow diagram of a

method of verifying an interface identifier of a DLAM routine's interface information of FIGURE 4. In FIGURE 7, the interface verifier 520 first performs initialization in a step 702.

After initialization, the interface verifier 520 retrieves the client's interface identifier from the associated types declaration file 420. In another embodiment of the present invention, the interface verifier 520 retrieves the interface identifier from the location in a DLAM where the interface identifier is coupled to the dynamically linkable routine or component.

Next, the interface verifier 520 retrieves the supplier's interface identifier history list. In an alternate embodiment of the present invention, the interface verifier 520 retrieves the supplier's interface identifier from any location associated with the supplier's interface identifier. In a third embodiment of the present invention, the supplier's interface identifier is a single interface identifier.

The interface verifier 520 then determines if the client's interface identifier is compatible with one of the interface identifiers in the supplier's list in a decisional step 710. In another embodiment of the present invention, the interface verifier 520 determines if the client's interface identifier is compatible with a single interface identifier associated with the supplier.

If the interface identifiers are compatible, the interface verifier 520 then proceeds with calling the dynamically linkable routine or component. If the interface identifiers are not compatible, the interface verifier 520 handles the interface incompatibility error and does not call the routine.

One skilled in the art should know that the present invention is not limited to the comparison of a software calling routine and a dynamically linkable routine or component within a DLAM. In another embodiment of the present invention, the interface verifier 520 can verify interface compatibilities of routines within the same DLAM or within other DLAMs. Also, other embodiments of the present invention may have additional or fewer steps than described above.

Although the present invention has been described in detail, those skilled in the art should understand that they can make various changes, substitutions and alterations herein without departing from the spirit and scope of the invention in its broadest form.

WHAT IS CLAIMED IS:

1. A system for validating an interface of a dynamically linkable component, comprising:

a check code generator that transforms said interface of said dynamically linkable component into an interface identifier representing said interface and couples said interface identifier to said dynamically linkable component; and

a interface verifier that employs said interface identifier to determine a compatibility of said dynamically linkable component.

2. The system as recited in Claim 1 wherein said check code generator transforms said interface of said dynamically linkable component into said interface identifier by transforming a textual representation of at least a portion of said interface.

3. The system as recited in Claim 1 wherein said check code generator couples said interface identifier to said dynamically linkable component by placing said interface identifier in a types declaration file.

4. The system as recited in Claim 1 wherein said interface
2 identifier varies as a function of a version of said dynamically
3 linkable component.

5. The system as recited in Claim 1 wherein said interface
2 verifier employs said interface identifier to determine a
3 compatibility of said dynamically linkable component with a second
4 dynamically linkable component.

6. The system as recited in Claim 1 wherein said interface
verifier is a part of a second dynamically linkable component.

7. The system as recited in Claim 1 wherein said interface
verifier determines a compatibility of said dynamically linkable
component by comparing said interface identifier with a history
list containing at least one member.

8. The system as recited in Claim 1 wherein said interface
2 identifier is a type selected from the group consisting of:
3 a check sum, and
4 a cyclic redundancy check.

9. The system as recited in Claim 1 wherein said check code
2 generator uses filtering directives to include and exclude portions
3 of said interface from said interface identifier.

569080" 05.05.00

10. A method of validating an interface of a dynamically
linkable component, comprising:

transforming said interface of said dynamically linkable
component into an interface identifier representing said interface;

coupling said interface identifier to said dynamically
linkable component; and

employing said interface identifier to determine a
compatibility of said dynamically linkable component.

11. The method as recited in Claim 10 wherein said
transforming comprises transforming a textual representation of at
least a portion of said interface.

12. The method as recited in Claim 10 wherein said coupling
comprises placing said interface identifier in a types declaration
file.

13. The method as recited in Claim 10 wherein said interface
identifier varies as a function of a version of said dynamically
linkable component.

14. The method as recited in Claim 10 wherein said employing
comprises employing said interface identifier to determine a
compatibility of said dynamically linkable component with a second
dynamically linkable component.

15. The method as recited in Claim 10 wherein said interface
verifier is a part of a second dynamically linkable component.

16. The method as recited in Claim 10 wherein said employing
comprises comparing said interface identifier with a history list
containing at least one member.

17. The method as recited in Claim 10 wherein said interface
identifier is a type selected from the group consisting of:

a check sum, and

a cyclic redundancy check.

18. The method as recited in Claim 10 wherein said
transforming uses filtering directives to include and exclude
portions of said interface from said interface identifier.

19. A system for validating an interface of a dynamically linkable component, comprising:

an interface identifier, coupled to said dynamically linkable component, that represents said interface of said dynamically linkable component; and

a interface verifier that employs said interface identifier to determine a compatibility of said dynamically linkable component.

20. The system as recited in Claim 19 wherein said interface identifier is contained within a types declaration file.

21. The system as recited in Claim 19 wherein said interface identifier varies as a function of a version of said dynamically linkable component.

22. The system as recited in Claim 19 wherein said interface verifier employs said interface identifier to determine a compatibility of said dynamically linkable component with a second dynamically linkable component.

23. The system as recited in Claim 19 wherein said interface verifier is a part of a second dynamically linkable component.

25. A method of validating an interface of a dynamically
linkable component, comprising:

coupling an interface identifier to said dynamically linkable
component; and

employing said interface identifier to determine a
compatibility of said dynamically linkable component.

26. The method as recited in Claim 25 wherein said coupling
comprises placing said interface identifier in a types declaration
file.

27. The method as recited in Claim 25 wherein said interface
identifier varies as a function of a version of said dynamically
linkable component.

28. The method as recited in Claim 25 wherein said employing
comprises employing said interface identifier to determine a
compatibility of said dynamically linkable component with a second
dynamically linkable component.

29. The method as recited in Claim 25 wherein said interface
verifier is a part of a second dynamically linkable component.

30. The method as recited in Claim 25 wherein said employing
2 comprises comparing said interface identifier with a history list
3 containing at least one member.

31. A real-time process control system, comprising:

a plurality of sensors and controllable devices;

a controller, coupled to said plurality of sensors and controllable devices, that executes software having at least first and second dynamically linkable components to coordinate an operation of said plurality of sensors and controllable devices;

an interface identifier, coupled to said first dynamically linkable component, that represents an interface of said first dynamically linkable component; and

a interface verifier that employs said interface identifier to determine a compatibility of said first and second dynamically linkable components.

32. The real-time process control system as recited in Claim 31 wherein said interface identifier is a transformation of a textual representation of at least a portion of said interface.

33. The real-time process control system as recited in Claim 31 wherein said interface identifier is contained within in a types declaration file.

34. The real-time process control system as recited in Claim
31 wherein said interface identifier varies as a function of a
version of said first dynamically linkable component.

35. The real-time process control system as recited in Claim
31 wherein said interface verifier is a part of said second
dynamically linkable component.

36. The real-time process control system as recited in Claim
31 wherein said interface verifier determines a compatibility of
said first dynamically linkable component by comparing said
interface identifier with a history list associated with said
second dynamically linkable component and containing at least one
member.

37. The real-time process control system as recited in Claim
31 wherein said interface identifier is a type selected from the
group consisting of:

a check sum, and

a cyclic redundancy check.

SYSTEM AND METHOD FOR VALIDATING INTERFACES
OF DYNAMICALLY LINKABLE COMPONENTS AND REAL-TIME PROCESS
CONTROL SYSTEM INCORPORATING THE SAME

ABSTRACT OF THE DISCLOSURE

A system for, and method of, validating an interface of a
dynamically linkable component, such that one dynamically linkable
component can assess its interface compatibility with another. In
one embodiment, the system includes: (1) a check code generator
that transforms the interface of the dynamically linkable component
into an interface identifier representing the interface and couples
the interface identifier to the dynamically linkable component and
(2) a interface verifier that employs the interface identifier to
determine a compatibility of the dynamically linkable component.

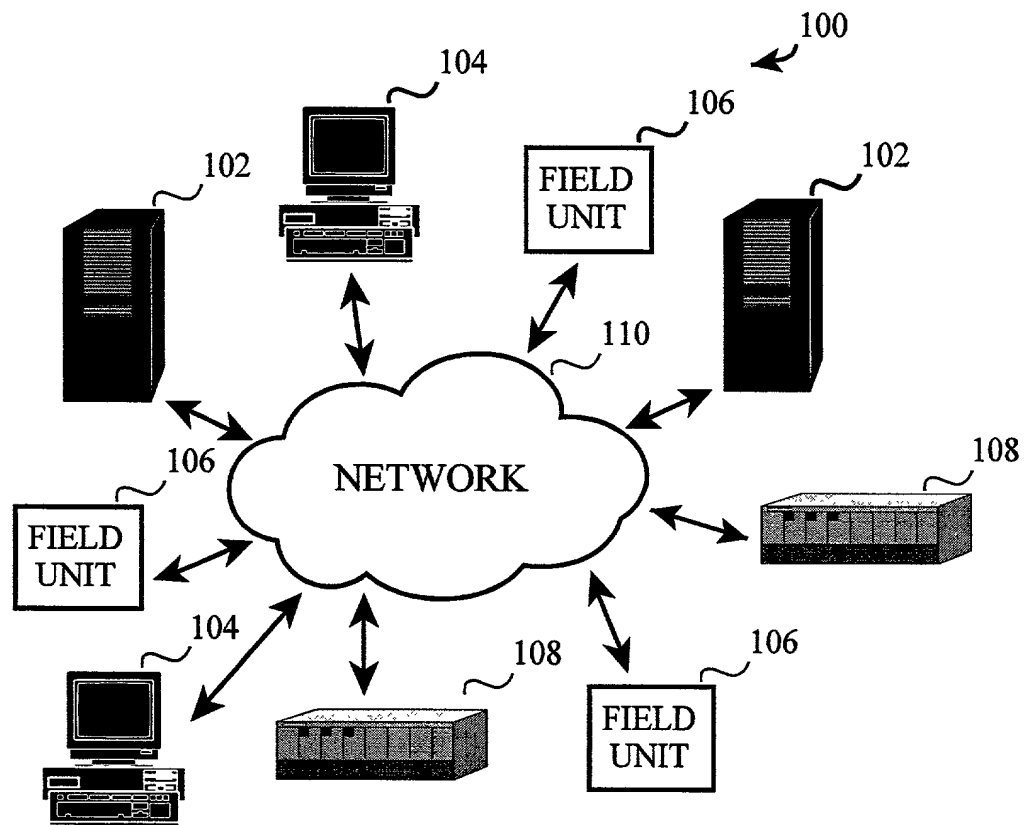


FIGURE 1

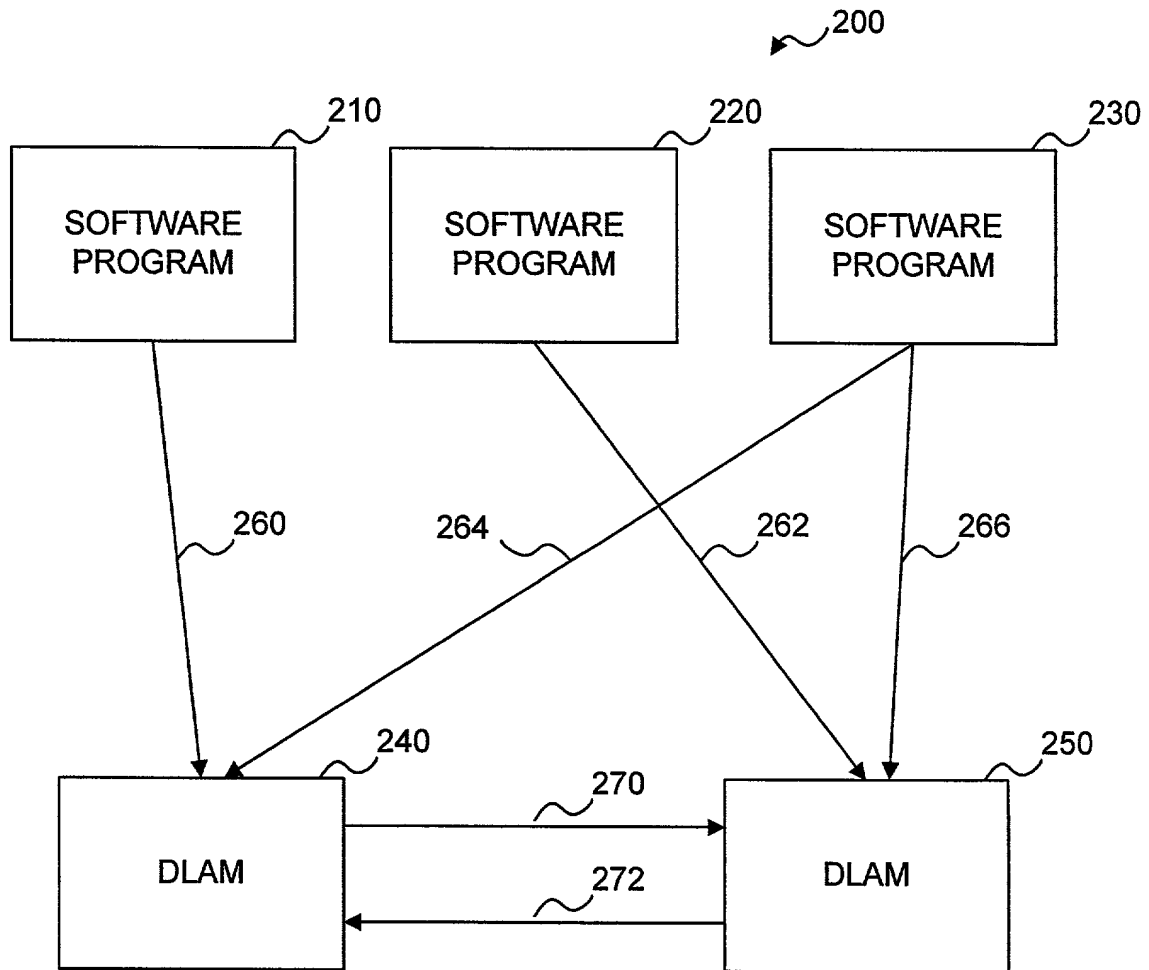


FIGURE 2

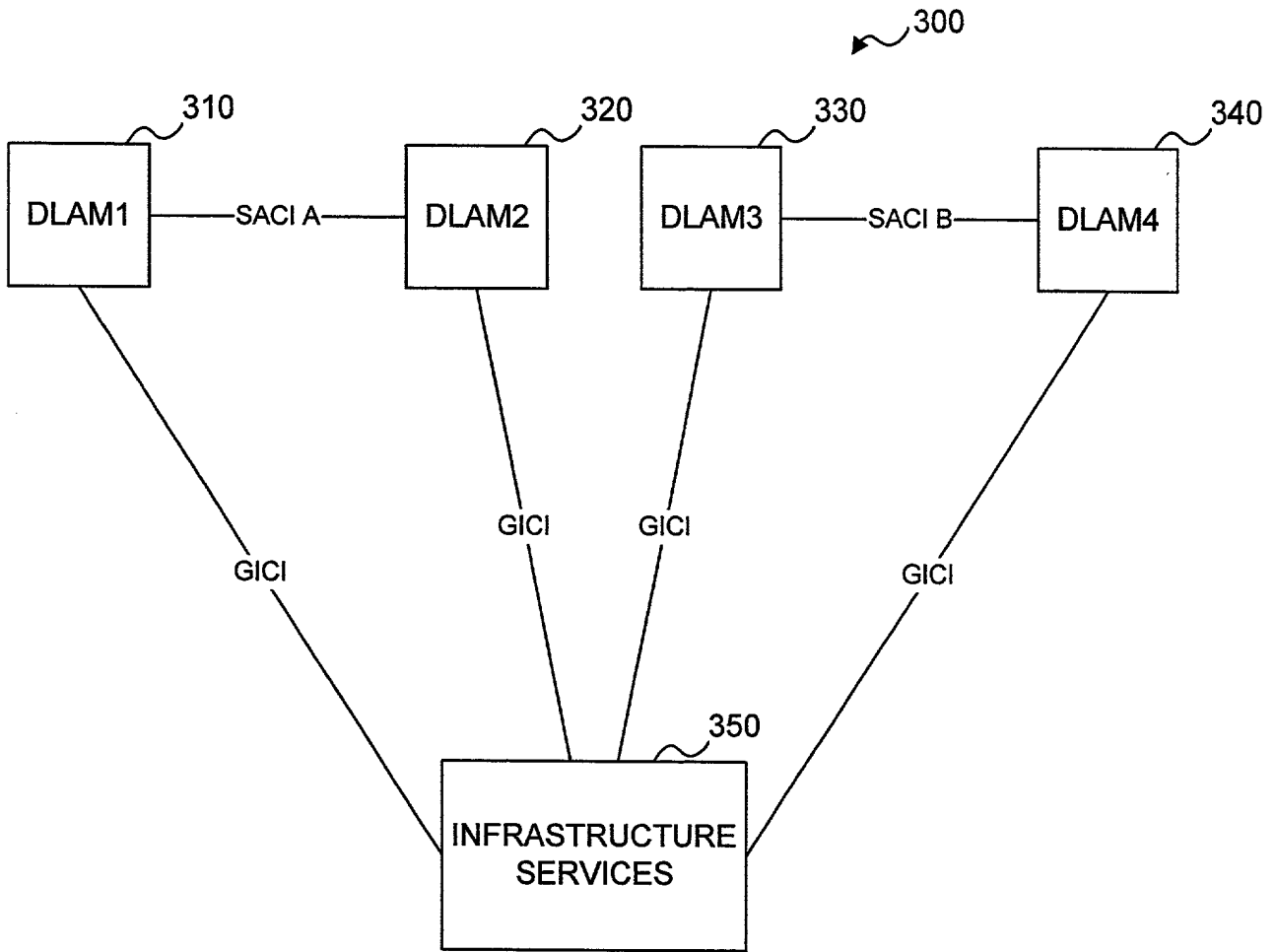


FIGURE 3

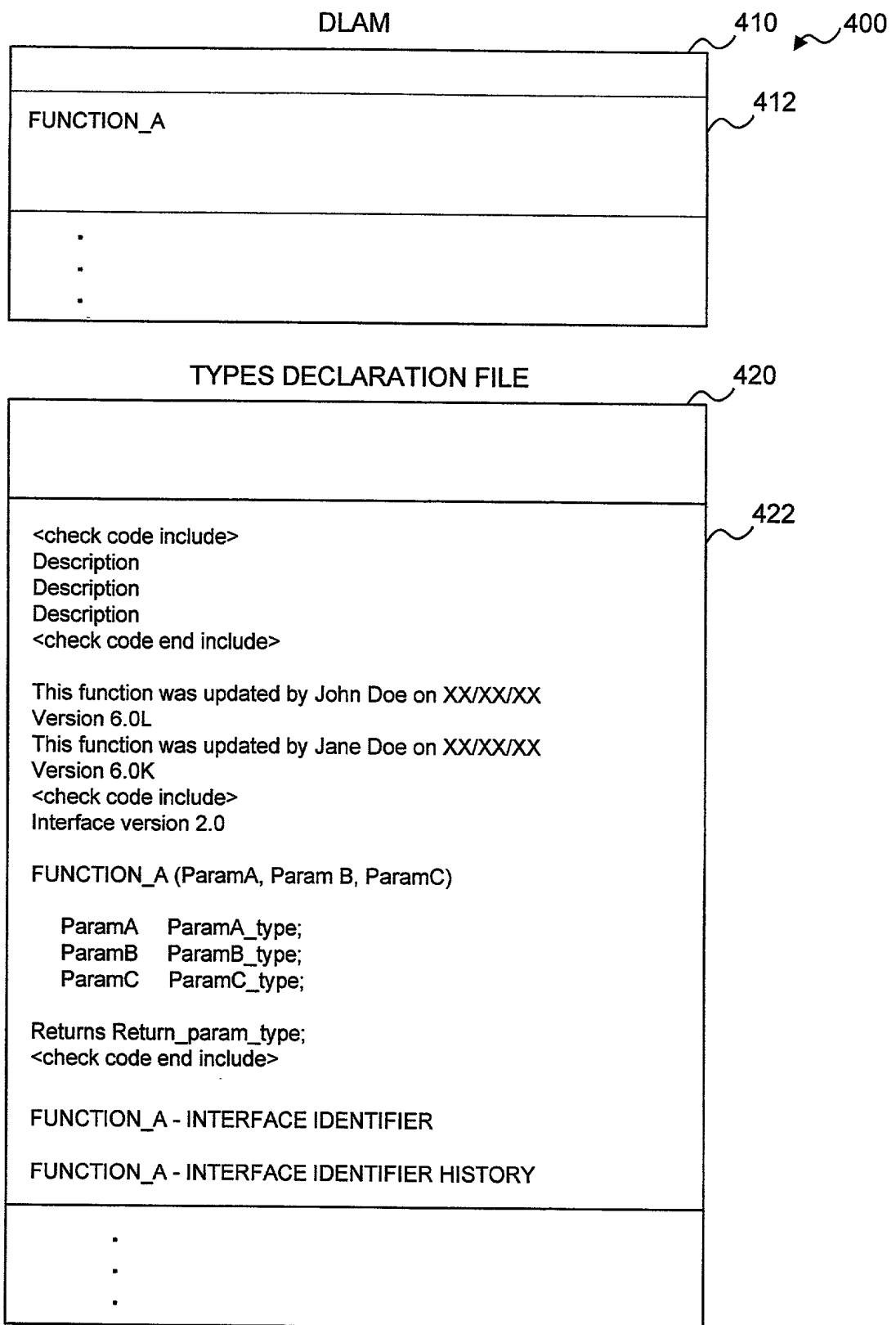


FIGURE 4

500

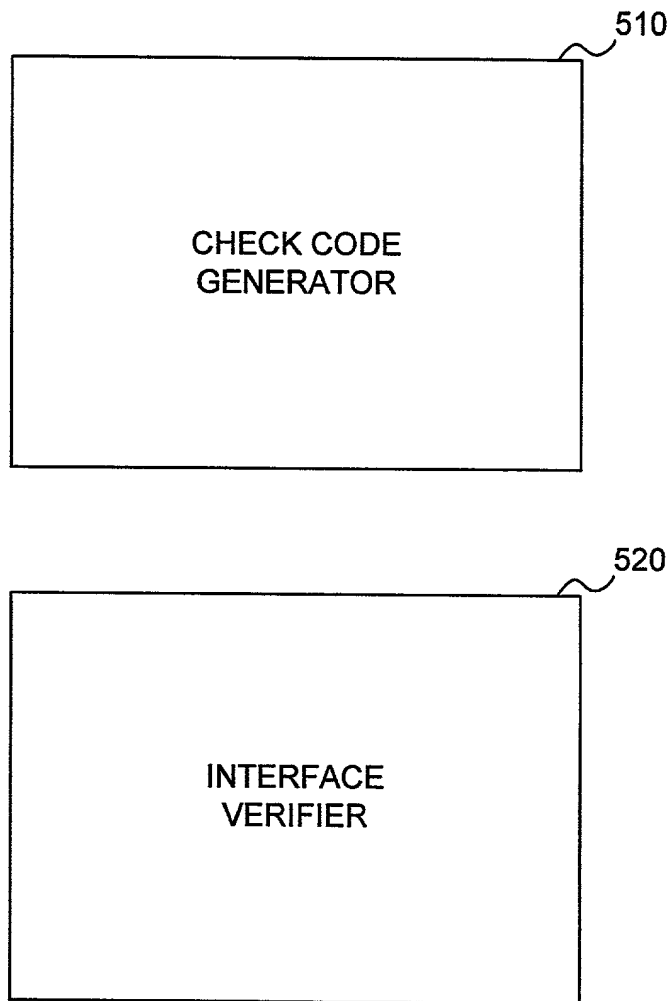


FIGURE 5

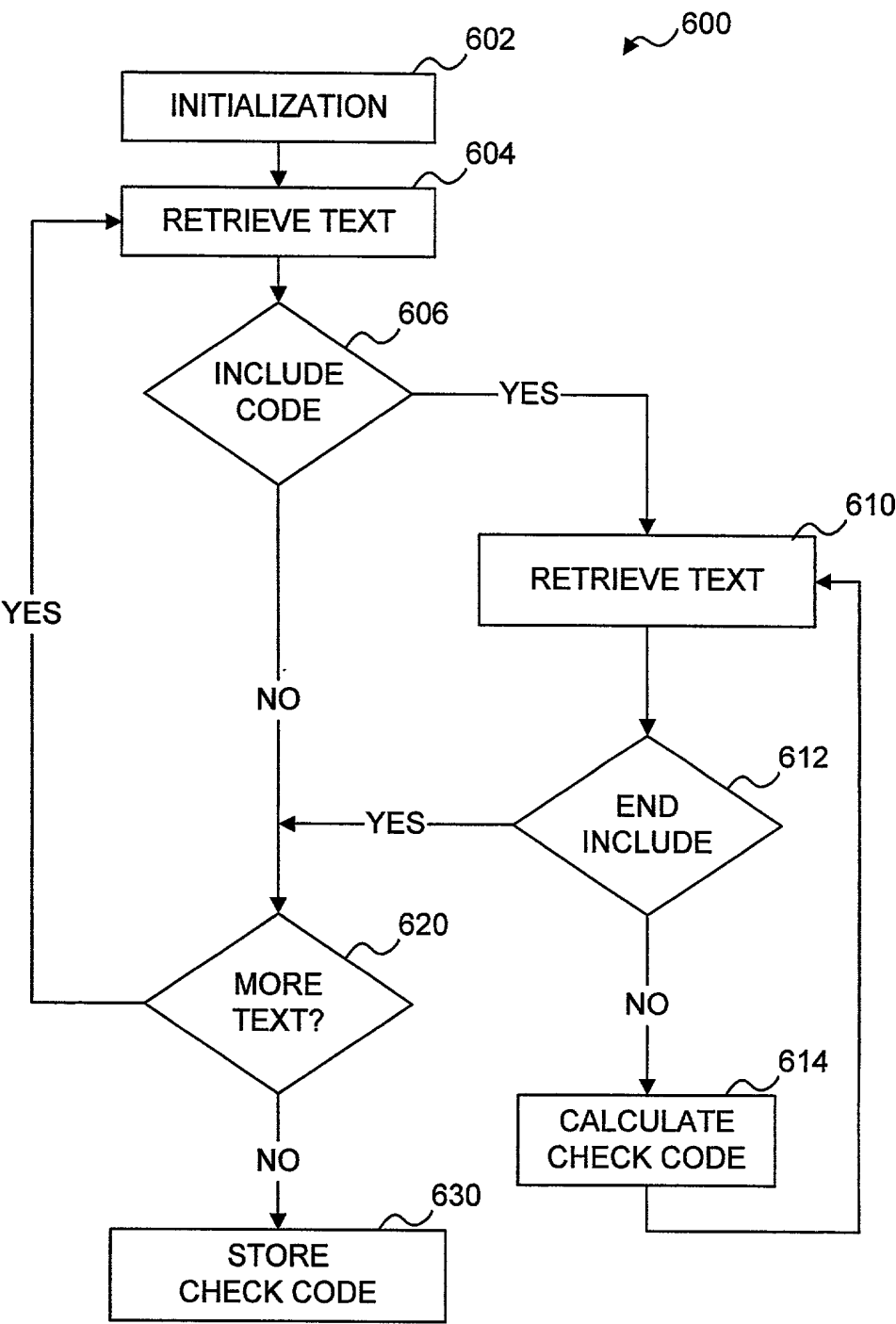


FIGURE 6

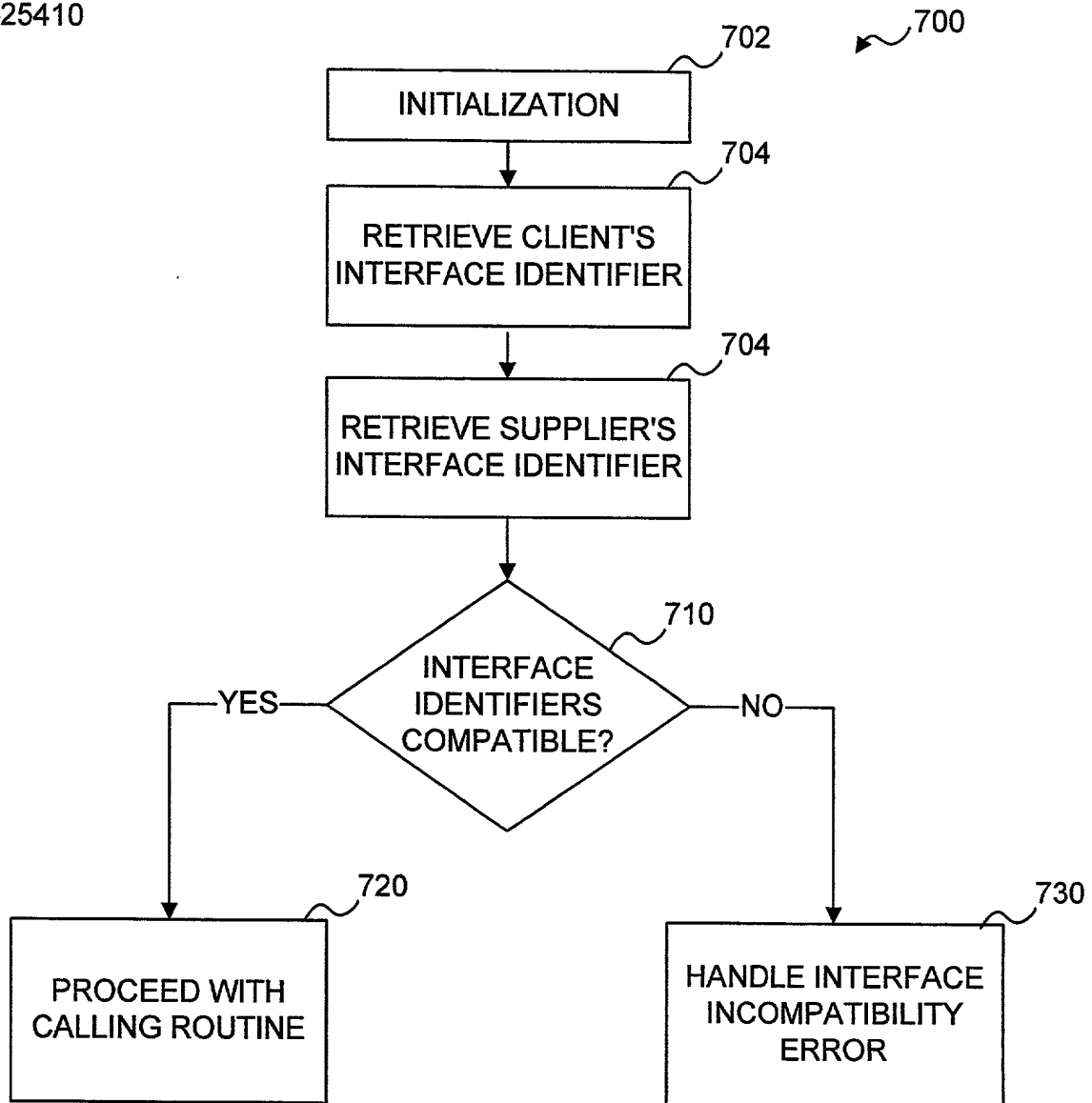


FIGURE 7

**IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE**

Declaration and Power of Attorney

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am an original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled "SYSTEM AND METHOD FOR VALIDATING INTERFACES OF DYNAMICALLY LINKABLE COMPONENTS AND REAL-TIME PROCESS CONTROL SYSTEM INCORPORATING THE SAME," the specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by an amendment, if any, specifically referred to in this oath or declaration.

I acknowledge the duty to disclose all information known to me which is material to patentability as defined in Title 37, Code of Federal Regulations, 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

None

I hereby claim the benefit under Title 35, United States Code, 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, 112, I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

None

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are

Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

I hereby appoint the following attorney(s) with full power of substitution and revocation, to prosecute said application, to make alterations and amendments therein, to receive the patent, and to transact all business in the Patent and Trademark Office connected therewith:

I hereby appoint:

Anthony Miologos, Registration No. 29,677

David H. Hitt, Registration No. 33,182

T. Ling Chwang, Registration No. 33,590

Charles W. Gaines, Registration No. 36,804

Glenn W. Boisbrun, Registration No. 39,615

Tung T. Nguyen, Registration No. 42,935

Please address all correspondence to Anthony Miologos, Honeywell Inc., 16404 North Black Canyon Highway, M/S AZ15/IE20, Phoenix, Arizona 85023. Telephone calls should be made to Anthony Miologos of Honeywell Inc. by dialing 602-313-5000.

Full name of first joint inventor: Jethro F. Steinman

Inventor's signature Jethro F. Steinman Date 8/3/99

Residence: City of Haverton
County of Delaware
State of Pennsylvania

Citizenship: USA

Post Office Address: 505 Dogwood Circle
Havertown, Pennsylvania 19083

Full name of second joint inventor: Brian R. Reynolds

Inventor's signature Brian R. Reynolds Date 8/3/99

Residence: City of Philadelphia
County of Philadelphia
State of Pennsylvania

Citizenship: USA

Post Office Address: 121 East Street
Philadelphia, Pennsylvania 19127